

J1000 U.S. PTO  
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U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10051886	FILING DATE 01/16/2002	CLASS 28	SUBCLASS 1	GAU 2827	EXAMINER <del>CRUZ</del>
<b>**APPLICANTS:</b> Felmetsger Valery; <i>Chambers</i>					
<b>**CONTINUING DATA VERIFIED:</b> AC None					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> AC None					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no				ATTORNEY DOCKET NO SPUTT-57354	
Verified and Acknowledged Examiners's initials <i>AC</i>					
TITLE : Permanent adherence of the back end of a wafer to an electrical component or sub-assembly <small>U.S. DEPT. OF COMM./PAT. &amp; TM-PTO-436L (Rev. 12-94)</small>					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
		Application Examiner	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	
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